



Attorney's Docket No. 004192.P053D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Min-hwa Chi

Application No. 09/865,929

Filed: May 24, 2001

For: TRANSISTOR AND LOGIC CIRCUIT)
ON THIN SILICON-ON-INSULATOR)
WAFERS BASED ON GATE INDUCED)
LEAKAGE CURRENTS)

Examiner: Kang, Donghee

Art Unit: 2811

Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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XX 37 C.F.R. §1.97(b).

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_____ A statement pursuant to 37 C.F.R. §1.97(e) or

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/10/03 
Jan Carol Little
Reg. No. 41,181

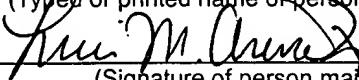
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231 on April 10, 2003.

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